

Encapsulation Technologies For Electronic Applications Materials And Processes For Electronic Applications

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Encapsulation Technologies For Electronic Applications

Encapsulation Technologies for Electronic Applications, Second Edition, offers an updated, comprehensive discussion of encapsulants in electronic applications, with a primary emphasis on the encapsulation of microelectronic devices and connectors and transformers. It includes sections on 2-D and 3-D packaging and encapsulation, encapsulation materials, including environmentally friendly 'green' encapsulants, and the properties and characterization of encapsulants.

Encapsulation Technologies for Electronic Applications ...

Encapsulation techniques used in electronic applications can be classified into five main technologies: molding, glob-topping, potting, underfilling, and printing. The selection of a suitable encapsulation method generally depends on several factors including equipment and labor cost, production volume, molding cycle, application requirements, package reliability, encapsulant material, and package type.

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Encapsulation Technologies for Electronic Applications is primarily focused on the encapsulation of microelectronic devices, with additional attention paid to the encapsulation of connectors and transformers.

Encapsulation Technologies for Electronic Applications

Underfill encapsulation process is conducted in the electronic packaging and manufacturing assembly sectors to improve both the reliability and performance of flip-chip package.

Encapsulation Technologies for Electronic Applications ...

Encapsulation Technologies for Electronic Applications (Materials and Processes for Electronic Applications) Electronics are used in a wide range of applications including computing, communication, biomedical, automotive, military and aerospace. They must operate in varying

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temperature and humidity environments including indoor controlled conditions and outdoor climate changes.

Encapsulation Technologies for Electronic Applications ...

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Encapsulation Technologies for Electronic Applications ...

incentives have stretched the application boundaries for plastic electronic packages. Many electronic applications that traditionally used hermetic packages such as military are now using commercial-off-the-shelf (COTS) plastic packages. Plastic encapsulation has the advantages of low cost, smaller form factors, and improved manufacturability.

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Encapsulant materials are also being developed for micro-electro-mechanical systems (MEMS), bio-MEMS, bio-electronics, and organic light-emitting diodes (O-LEDs). This book offers a comprehensive...

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Encapsulation Technologies for Electronic Applications ()

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