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The Aldec Active-HDL Student Edition is also available packaged with Digital Systems Design Using VHDL from Brooks/Cole. All of the examples in the book should compile and simulate correctly using Active-HDL version 3.5 Student Edition, with the exception of the 6805 microcontroller example in Appendices D and E.

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checkouts, the TAs will apply several input combinations to test your design.

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Describing a Design. In VHDL an entity is used to describe a hardware module. An entity can be described using, Entity declaration

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Lizy John is the B. N. Gafford Professor in Electrical and Computer Engineering at the University of Texas at Austin. Dr. John has been teaching and conducting research in computer architecture and digital systems design for almost two decades. She has coauthored DIGITAL SYSTEMS

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